Reaching New Levels of Linearity in Passive Mixers with GaN Technology

By Charles Trantanella, Custom MMIC Chief Scientist
When microwave engineers hear the words “Gallium Nitride” (GaN), one thought usually comes to their minds: power amplifiers. Due to its physical properties of high-breakdown voltage and high-thermal conductivity, GaN is ideally suited over its Gallium Arsenide (GaAs) counterpart for the development of highly efficient, highly effective power amplifiers. Until now, the benefits of GaN technology beyond amplification have not been pursued.

Over the past year, Custom MMIC has been working with a few domestic foundries to investigate the use of GaN for passive mixers. As a result of this work, we have discovered the high-linearity properties of GaN amplifiers do indeed translate to mixers. This realization has let us develop a new family of ultra-linear mixers operating from 1 to 20 GHz. With these mixers, microwave engineers can now approach levels of linearity that used to seem impossible.

Current state of affairs

The passive, high-frequency mixer market of today is dominated by circuits constructed from various GaAs processes including MESFET, pHEMT, and HBT. A key linearity-performance criterion used to judge different mixers is the third-order intercept point (IP3), as the higher the IP3, the better the linearity. One key factor that can influence the IP3 is the LO drive level. Many GaAs mixers achieve an IP3 that is only slightly higher than the LO drive level. To examine this phenomenon, we define the new metric, **Linear Efficiency**, which is the difference between IP3 and LO drive level. In Table 1 below, we summarize the linear efficiencies of some commercially available mixers.

In this table, we note that many of the mixers have a linear efficiency of 3 to 8 dB, with no clear dependence on frequency or process. Such linearity performance has been relatively unchanged over the past twenty years, when GaAs-based MMIC mixers were first introduced into the marketplace. Since then, system linearity requirements have only increased. Today, microwave engineers are routinely asking for mixers with IP3 levels above 30 dBm, if not 40 dBm. Based on the information in Table 1, GaAs-based mixers would require extraordinary amounts of LO power, greater than 30 dBm, to achieve these high-linearity levels. Such a cost is usually prohibitive in most systems, and is one reason why highly linear GaAs mixers have not been introduced to the market.

<table>
<thead>
<tr>
<th>Technology</th>
<th>Topology</th>
<th>LO, RF Freq. (GHz)</th>
<th>IF Freq. (GHz)</th>
<th>Conv. Gain (dB)</th>
<th>LO Drive (dBm)</th>
<th>Input IP3 (dBm)</th>
<th>Linear Efficiency (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>pHEMT</td>
<td>Double balanced</td>
<td>2.5 - 7</td>
<td>DC - 3</td>
<td>-9</td>
<td>13</td>
<td>18</td>
<td>5</td>
</tr>
<tr>
<td>pHEMT</td>
<td>Double balanced</td>
<td>3 - 10</td>
<td>DC - 4</td>
<td>-9</td>
<td>17</td>
<td>24</td>
<td>7</td>
</tr>
<tr>
<td>pHEMT</td>
<td>Double balanced</td>
<td>3.5 - 8</td>
<td>DC - 1.6</td>
<td>-7</td>
<td>13</td>
<td>17</td>
<td>4</td>
</tr>
<tr>
<td>MESFET</td>
<td>Double balanced</td>
<td>5.5 - 14</td>
<td>DC - 6</td>
<td>-7.5</td>
<td>15</td>
<td>21</td>
<td>6</td>
</tr>
<tr>
<td>HBT</td>
<td>Double balanced</td>
<td>6 - 14</td>
<td>DC - 4.5</td>
<td>-6.5</td>
<td>13</td>
<td>16</td>
<td>3</td>
</tr>
<tr>
<td>HBT</td>
<td>Double balanced</td>
<td>16 - 26</td>
<td>DC - 9</td>
<td>-6.5</td>
<td>13</td>
<td>17</td>
<td>4</td>
</tr>
<tr>
<td>MESFET</td>
<td>Double balanced</td>
<td>18 - 32</td>
<td>DC - 8</td>
<td>-9</td>
<td>13</td>
<td>19</td>
<td>6</td>
</tr>
<tr>
<td>HBT</td>
<td>Double balanced</td>
<td>20 - 32</td>
<td>DC - 10</td>
<td>-7</td>
<td>13</td>
<td>18</td>
<td>5</td>
</tr>
<tr>
<td>pHEMT</td>
<td>Double balanced</td>
<td>24 - 40</td>
<td>DC - 17</td>
<td>-8</td>
<td>13</td>
<td>21</td>
<td>8</td>
</tr>
</tbody>
</table>

Table 1: Summary of some commercially available, passive, double-balanced GaAs MMIC mixers, including their linear efficiencies.
GaN to the rescue

As developers of passive GaAs mixers, we at Custom MMIC have studied this problem in detail and asked what GaN can do to help solve this problem. One compelling feature of GaN processes is that they can generally support two types of mixing elements: a 2-terminal Schottky diode, and a 3-terminal FET. Each of these mixing devices offers its own unique benefits for increased linearity through physical properties and topology choice. Consider the 2-terminal GaN Schottky diode. In Figure 1, we present a typical DC I-V trace for a GaN diode as compared to a typical GaAs pHEMT diode.

In this figure, we note the pHEMT diode has a turn-on voltage of approximately 0.8 V, whereas the GaN diode has a turn-on voltage closer to 1.1 V. A higher turn-on voltage translates directly into a higher IP3 level, but at a cost of more LO drive. However, as we will see, the GaN diodes deliver a much higher LO efficiency than their GaAs pHEMT counterparts. Although not shown, a similar trend can be seen in the three terminal GaN FET devices, which have a more negative pinch-off voltage and higher breakdown voltage than their GaAs counterparts. Such performance allows for higher levels of linearity in passive mixers, again at the cost of higher LO drive.

FIGURE 1.
DC I-V comparison of a typical GaAs pHEMT diode versus a GaN diode.
**GaN mixer higher linearity and superior efficiency**

Custom MMIC has been investigating GaN-based mixers for the past few years on a number of different processes and foundries. Currently, we have developed a family of highly linear mixers with input IP3 levels well above 30 dBm and linear efficiencies of greater than 10 dB. A summary of these designs is shown below in *Table 2*.

In the sections below, we take a closer look at three of the most recent mixers, which are highlighted in the table below.

<table>
<thead>
<tr>
<th>Technology</th>
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</thead>
<tbody>
<tr>
<td>GaN</td>
<td>Diplexed FET</td>
<td>1.4 - 2.6</td>
<td>DC - 0.8</td>
<td>-8</td>
<td>21</td>
<td>35</td>
<td>14</td>
</tr>
<tr>
<td><strong>GaN</strong></td>
<td><strong>Diplexed FET</strong></td>
<td><strong>2.6 - 3.8</strong></td>
<td><strong>DC - 1.1</strong></td>
<td><strong>-8</strong></td>
<td><strong>22</strong></td>
<td><strong>37</strong></td>
<td><strong>15</strong></td>
</tr>
<tr>
<td>GaN</td>
<td>Double balanced</td>
<td>6 - 11</td>
<td>DC - 5</td>
<td>-6</td>
<td>19</td>
<td>30</td>
<td>11</td>
</tr>
<tr>
<td><strong>GaN</strong></td>
<td><strong>Double balanced</strong></td>
<td><strong>7 - 13</strong></td>
<td><strong>DC - 1.5</strong></td>
<td><strong>-8</strong></td>
<td><strong>22</strong></td>
<td><strong>35</strong></td>
<td><strong>13</strong></td>
</tr>
<tr>
<td>GaN</td>
<td>Single ended</td>
<td>13 - 19</td>
<td>DC - 2</td>
<td>-9</td>
<td>24</td>
<td>34</td>
<td>10</td>
</tr>
</tbody>
</table>

*Table 2: Summary of GaN mixers developed by Custom MMIC, showing very high linear efficiencies.*

**S-band GaN FET mixer (3-terminal device)**

The first design is a 3-terminal FET mixer as shown in *Figure 2*. The mixer has an operational bandwidth of 2.6 to 3.8 GHz and an IF response from DC to 1.1 GHz.

In this figure, we note the LO is applied to the gate through a filter network, whereas the RF/IF is applied to the drain via a diplexer constructed from a spiral transformer. The transformer does not introduce any balance into the circuit, but does allow for an enhanced isolation of the signals. Additional filtering elements in the signal path provide further port-to-port isolations.
In *Figure 3*, we present the conversion gain for the S-band mixer as a function of frequency. Here, the mixer was measured as a downconverter with a fixed IF frequency of 100 MHz, USB. We note the mixer has a conversion gain of approximately -8 dB across the operating bandwidth when driven by an LO signal greater than 20 dBm.

![Figure 3](image)

*FIGURE 3.*

Conversion gain of the S-band GaN mixer. IF=100 MHz USB.

In *Figure 4*, we present the input IP3 of the S-band mixer at a number of LO drive levels. We note that at a drive level of +26 dBm, the input IP3 averages around +40 dBm, which demonstrates a linear efficiency of nearly 14 dB.

**X-Band GaN diode mixer (2-terminal device)**

The second mixer is a double-balanced, diode-based mixer, as shown below in *Figure 5*. The operational bandwidth of this mixer is 7 to 14 GHz with an IF response range of DC to 1.5 GHz.

![Figure 5](image)

*FIGURE 5.*

Die photograph of the X-band GaN diode mixer.

In this figure, we note the LO and RF signals are fed to the diode ring through spiral balun transformers, while the IF signal is extracted from the centertap of the RF balun. Additional matching and filter elements are also added to each of the ports to optimize the performance.
In Figure 6, we present the conversion gain of the X-band mixer versus frequency. Here, the mixer was configured as a downconverter with a fixed IF frequency of 100 MHz LSB. We note the mixer has a conversion gain of approximately -8 dB across the operating bandwidth when driven with an LO drive level greater than 16 dBm.

In Figure 7, we present the input IP3 of the X-band mixer at a number of LO drive levels. We note that at drive levels above +22 dBm, the input IP3 is +30 to +38 dBm, which demonstrates a linear efficiency of 8 to 12 dBm.

Ku-band FET mixer (3-terminal device)

Our third mixer is a single-ended, cold FET mixer, as shown below in Figure 8. The operational bandwidth of this mixer is 13 to 19 GHz with an associated IF response range of DC to 2 GHz.

In this figure, we note the LO is applied to the gate of the GaN FET through a filter network, whereas the RF and IF signals are applied to, and extracted from, the drain through high pass (RF) and low pass (IF) networks. These filter networks provide the isolation in the mixer, as the topology has no inherent balance. The source of the FET is grounded.
In Figure 9, we present the conversion gain of the Ku-band mixer versus frequency. Here, the mixer was configured as a downconverter with a fixed IF frequency of 100 MHz USB. We note the mixer has a conversion gain of approximately -9 dB across the operating bandwidth for LO drive levels above +22 dBm.

In Figure 10, we present the input IP3 of the Ku-band mixer at a number of LO drive levels. We note the input IP3 increases as the LO drive level increases, up to a measured value of +35 dBm. This does not appear to be the ultimate limit. Unfortunately, we were constrained in LO drive during this measurement and were unable to generate higher power. Regardless, the Ku-band FET mixer demonstrates a linear efficiency of 8 to 10 dB.

Where do we go from here?

Based on the results for these three examples, it is clear that GaN mixers can deliver linear efficiencies that are much higher than their GaAs counterparts. The use of 2-terminal or 3-terminal mixing devices offers a wide range of mixer topology possibilities, thus allowing GaN mixers to be optimized for many different microwave systems. At Custom MMIC, we have already begun developing GaN mixers to cover more of the frequency spectrum, while still retaining very high linear efficiencies. We expect these mixers will soon find their way into radar communications and instrumentation systems near and far.

Next Steps

Complete Mixer Data
Support and Technical Resources

Custom MMIC strives to make success achievable through innovative MMIC design, thorough product specifications, and fast delivery of evaluation boards. Learn more at www.custommmic.com.